In re	U.S. Patent Application of	
TAK	AHASHI et al.	
)	Art Unit 2817
Appli	cation Number: 10/811,388	
		Examiner
Filed:	: March 29, 2004	Nguyen, Khanh, V.
For:	Semiconductor Integrated Circuit for) High Frequency Power Amplifier,)	
	ELECTRONIC COMPONENT FOR HIGH FREQUENCY)	
	POWER AMPLIFIER, AND RADIO	
	COMMUNICATION SYSTEM)	
)	
Attor	ney Docket No. HITA.0533	

Honorable Assistant Commissioner for Patents Washington, D.C. 20231

INFORMATION DISCLOSURE STATEMENT

Sir:

Pursuant to 37 C.F.R. §§ 1.56 and 1.97, this Information Disclosure Statement is submitted in the above-identified patent application. A listing of documents to be published on the face of any patent granted from this application is submitted herewith on Form Any other documents or information submitted for consideration by the Examiner are listed in this paper. A copy of each foreign patent, or each publication or portion thereof listed or herein identified, is submitted herewith.

CERTIFICATION

This Information Disclosure Statement is submitted after three months from the filing date of the above-identified U.S. patent application and after the mailing date of the first Office Action on the merits of the above-identified application. The fee required under 37 C.F.R. 1.97(c) is submitted herewith.

The Examiner is requested to acknowledge consideration of the information provided in this paper in accordance with prescribed procedures.

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Please charge any additional fees or credit any overpayments in connection with this paper to Deposit Account No. 08-1480.

Respectfully submitted,

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U.S. Serial Number 09/555,010 filed on 5/23/2000, in Japanese (34 pages), along wit						with a verifie	d English				
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EXAMINER: Initial if citation is considered, whether or not citation is in conformance with MPEP 609; draw a line through											
citation if not in conformance and not considered. Include copy of this form with next communication to applicant											

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